**Amendments to the Claims:**

This listing of claims will replace all prior version, and listings, of claims in the application:

**Listing of Claims:**

Claims 1 and 2. (Canceled).

3. (New) A data processing device, comprising:

a cache memory;

lines leading to the cache memory;

a data processing logic cell field; and

at least one sequential central processing unit (CPU) coupled via the lines to the data processing logic cell field for data exchange.

4. (New) The data processing device of claim 3, wherein the data exchange is in block form.

5. (New) A method for operating a reconfigurable unit having runtime-limited configurations, comprising:

increasing, by a configuration, the configuration's maximum allowed runtime;

wherein the increasing is suppressed in response to an interrupt.

6. (New) The method of claim 5, wherein the configuration triggers a parallel counter to perform the increasing.